REVISIONS

LTR	DESCRIPTION	DATE	APPROVED
A	Pgs 4, 5: Corrected Table I and deleted some unnecessary testing at 5V/10 V biases. Pg 7: Added Cond. A to Burn-in; separated C_{in} test in 4.4.1. Pg 8: Corrected Table II. Pgs 9, 10: Added Military replacement P/N. CRJ	3-17-78	MCWhanel
B	Pg 10: Corrected similar vendor type numbers; and complete document editorial revision.	9-28-81	MCWhanel
C	Pg 1: Document Inactive	7-20-82	MCWhanel

Prepared in accordance with MIL-STD-100

Selected item drawing

REV	C	B	B	B	B		B	B	B	B	B
PAGE	1	2	3	4	5	6	7	8	9	10	11
REV STATUS OF PAGES	REV										
	PAGES										

Original Date of Drawing
13 September 1977

PREPARED BY: A. J. Foley 6 Sept 77

CHECKED BY: C. R. Jackson 9-12-77

APPROVED BY: MCWhanel 13 Sep 77

DEFENSE ELECTRONICS SUPPLY CENTER
DAYTON, OHIO

TITLE: Microcircuits, Digital, CMOS, HEX Inverter, Monolithic Silicon

SIZE	CODE IDENT. NO.	DWG NO.
A	14933	77027

REV C PAGE 1 OF 11

DESC FORM 144 MAR 76

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